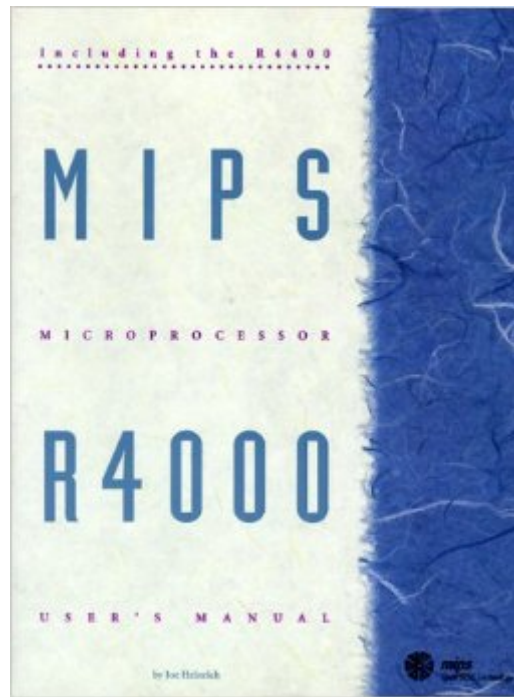


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MIPS R4000 User's Manual



Synopsis

This manual is a comprehensive reference describing the implementation-specific interfaces and architectural features of the highly-integrated 64-bit R4000 and R4400 MIPS RISC processors. This manual also describes the MIPS RISC instruction Set Architecture (ISA), including the 64-bit extensions of the ISA.

Book Information

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This is an official book that was published by MIPS and Joseph Heinrich was an employee of MIPS when he wrote the book. This one book teaches you everything necessary in order to program the MIPS series of processors. This is the definitive book on the 32-bit CPU instruction set, plus 64-bit extensions. It's one of the best books on a microprocessor I've ever read. It includes everything - memory management, cpu exceptions, cache organization, etc., plus a full description of the instruction set, suitable as a reference for assembly-language programmers. I used this book while teaching a class on computer architecture at the University of Illinois. You can get a free PDF copy of this manual from the MIPS web site, however, I'd prefer to get the hardback or the paperback (it's grey - but out of print,). I don't even program the MIPS CPU any more but I lost my paperback and I want another since the MIPS chip is a very significant chip in the history of computer architecture.

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